

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	56	703/13.ccls. and @pd>"20070401"	US-PGPUB; USPAT; EPO; DERWENT	OR	ON	2007/08/03 16:44
L2	6669	(virtual adj machine) and @ad<"20020101"	US-PGPUB; USPAT; EPO; DERWENT	OR	ON	2007/08/03 16:45
L3	2724	L2 and translat\$5	US-PGPUB; USPAT; EPO; DERWENT	OR	ON	2007/08/03 16:46
L4	2120	L3 and (operating adj system)	US-PGPUB; USPAT; EPO; DERWENT	OR	ON	2007/08/03 16:46
L5	1218	L4 and (trap\$1 or exception\$1 or interrupt\$1)	US-PGPUB; USPAT; EPO; DERWENT	OR	ON	2007/08/03 16:47
L6	667	L5 and (simulat\$5 or emulat\$5)	US-PGPUB; USPAT; EPO; DERWENT	OR	ON	2007/08/03 16:47


[Web](#) [Images](#) [Video](#) [News](#) [Maps](#) [more »](#)

virtual machine simulation

1950

- 2001

Search

Ad
Sc
Sc

Scholar All articles - **Recent articles** Results 1 - 10 of about 40,200 for **virtual machine simulation**.

All Results

[J Gosling](#)[T Lindholm](#)[K Arnold](#)[F Yellin](#)[J Craig](#)

[book] **Java Virtual Machine Specification - all 20 versions »**

T Lindholm, F Yellin - 1999 - Addison-Wesley Longman Publishing Co., Inc. Boston, MA, USA

... Matthew Wilding , David Greve , David Hardin, Efficient **Simulation** of Formal ... Michael Haupt , Mira Mezini , Klaus Ostermann, **Virtual machine** support for dynamic ...

Cited by 2540 - [Related Articles](#) - [Web Search](#) - [Library Search](#)

Embra: fast and flexible machine simulation - all 7 versions »

E Witchel, M Rosenblum - Proceedings of the 1996 ACM SIGMETRICS international ..., 1996 - portal.acm.org

... instructions and access data at the **virtual** address specified ... the CPU in a real **machine**,

efficient modeling ... we present the fast MMU **simulation** techniques used ...

Cited by 202 - [Related Articles](#) - [Web Search](#) - [BL Direct](#)

[book] **Shade: a fast instruction-set simulator for execution profiling - all 21 versions »**

B Cmelik, D Keppel - 1994 - ACM Press New York, NY, USA

... to the **virtual** target **machine**, or where we wish to virtualize the target **machine** operations, several instructions, or even a call to a **simulation** function may ...

Cited by 532 - [Related Articles](#) - [Web Search](#) - [Library Search](#) - [BL Direct](#)

Using the SimOS Machine Simulator to Study Complex Computer Systems - all 9 versions »

M ROSENBLUM, E BUGNION, S DEVINE, SA HERROD - ACM Transactions on Modeling and Computer **Simulation**, 1997 - portal.acm.org

... The accurate **simulation** models of SimOS are heavily ... timing information describing the simulated **machine's** execution ... address back to the **virtual** address of ...

Cited by 240 - [Related Articles](#) - [Web Search](#) - [BL Direct](#)

VM/4: ACOS-4 virtual machine architecture

S Nanba, N Ohno, H Kubo, H Morisue, T Ohshima, H ... - ACM SIGARCH Computer Architecture News, 1985 - portal.acm.org

... VM/% introducing high-speed I/O operations called VM mode I/O applied to the channel programs on the **virtual machine** reduces channel program **simulation** overhead ...

Cited by 33 - [Related Articles](#) - [Web Search](#)

Virtual machine monitors for scalable multiprocessors - all 3 versions »

E Bugnion, SW Devine, M Rosenblum - US Patent 6,075,938, 2000 - Google Patents Page 1. [54] **VIRTUAL MACHINE MONITORS FOR SCALABLE MULTIPROCESSORS** ... 711/122 OTHER

PUBLICATIONS Goldberg, R., Survey of **Virtual Machine** Research, Computer, pp. ...

Cited by 38 - [Related Articles](#) - [Web Search](#)

[book] **The Java programming language - all 7 versions »**

K Arnold, J Gosling - 1998 - ACM Press/Addison-Wesley Publishing Co. New York, NY, USA

... of the 31st conference on Winter **simulation: Simulation**—a bridge to the ... Laurent


[Web](#) [Images](#) [Video](#) [News](#) [Maps](#) [more »](#)

virtual machine emulation

1950

- 2001

Search

Ad
Sc
Sc

Scholar All articles - **Recent articles** Results 1 - 10 of about 5,130 for **virtual machine emulation**. ((

All Results[J Babb](#)[A Agarwal](#)[R Tessier](#)[D Golub](#)[R Rashid](#)**A virtual machine emulator for performance evaluation**

MD Canon, DH Fritz, JH Howard, TD Howell, MF ... - Communications of the ACM, 1980 - portal.acm.org

... two environments: the environment normally presented by CP which we will call the standard **virtual machine** environment, and the **emulation** environment described ...Cited by 17 - [Related Articles](#) - [Web Search](#)**Optimising Bytecode Emulation for Prolog**

VS Costa - Proceedings of the International Conference PPDP'99 on ..., 1999 - portal.acm.org

... Optimising Bytecode **Emulation** for Prolog. ... Konstantinos Sagonas, Instruction merging and specialization in the SICStus Prolog **virtual machine**, Proceedings of the ...Cited by 45 - [Related Articles](#) - [Web Search](#)**Virtual machine monitors for scalable multiprocessors - all 3 versions »**

E Bugnion, SW Devine, M Rosenblum - US Patent 6,075,938, 2000 - Google Patents

... The present invention uses a combination of innovative **emulation** of the DMA ... Disco, a **virtual machine** monitor, is a software layer between a multiprocessor ...Cited by 38 - [Related Articles](#) - [Web Search](#)**Virtual and emulated objects for use in the user interface of a display screen of a display ... - all 2 versions »**

MH Beard, PA Caro, JB Hsiao, KJ Mackey, JG Sandman ... - US Patent 4,939,507, 1990 - Google Patents

... BOOT* SHOWING OPTIONS: OK TO CHANGE DEVICE/ DRIVE CONFIGURATION NOT OK TO LOAD **VIRTUAL**MEDIA. ISO ... 170 772 \ - 1 SELECT PC **EMULATOR** ICON AND PRESS <PROPS> 174 ...Cited by 100 - [Related Articles](#) - [Web Search](#)**Logic emulation with virtual wires - all 15 versions »**

J Babb, R Tessier, M Dahl, SZ Hanono, DM Hoki, A ... - Computer-Aided Design of Integrated Circuits and Systems, ..., 1997 - ieeexplore.ieee.org

... Abstract—Logic **emulation** enables designers to functionally verify ... use of expensive crossbar and PC-board technology ... **Virtual** wires overcome pin limitations by ...Cited by 69 - [Related Articles](#) - [Web Search](#) - [BL Direct](#)**Static grouping of small objects to enhance performance of a paged virtual memory**

JW Stamos - ACM Transactions on Computer Systems (TOCS), 1984 - portal.acm.org

... 2.2 **Virtual Machine Emulation** Exact duplication of the timing and nature of user input from the keyboard and pointing devices is impossible. ...Cited by 79 - [Related Articles](#) - [Web Search](#)**Contemporary Concepts of Microprogramming and Emulation**

RF Rosin - ACM Computing Surveys (CSUR), 1969 - portal.acm.org

... For example, it is possible to emulate any of a family of **virtual** machines with byte ... However, the same **machine** may not be at all suitable for **emulation** of a ...



Welcome United States Patent and Trademark Office

☐ Search Results

[BROWSE](#)
[SEARCH](#)
[IEEE XPLORE GUIDE](#)

Results for "((virtual machine<and>simulation)) <and> (pyr >= 1913 <and> pyr <= 2001)"

Your search matched 1373 of 1625854 documents.

A maximum of 100 results are displayed, 25 to a page, sorted by Relevance in Descending order.

e-mail

» Search Options

[View Session History](#)
[New Search](#)

» Key

IEEE JNL	IEEE Journal or Magazine
IET JNL	IET Journal or Magazine
IEEE CNF	IEEE Conference Proceeding
IET CNF	IET Conference Proceeding
IEEE STD	IEEE Standard

Modify Search

☐ Check to search only within this results set

 Display Format: ☒ Citation ☐ Citation & Abstract

 [Select All](#) [Deselect All](#)

View: 1-25 | :

- | | |
|--------------------------|---|
| <input type="checkbox"/> | <p>1. A retargetable, ultra-fast instruction set simulator
 Jianwen Zhu; Gajski, D.D.;
 <u>Design, Automation and Test in Europe Conference and Exhibition 1999. Proceedings</u>
 9-12 March 1999 Page(s):298 - 302
 Digital Object Identifier 10.1109/DATE.1999.761137
 AbstractPlus Full Text: PDF(64 KB) IEEE CNF
 Rights and Permissions</p> |
| <input type="checkbox"/> | <p>2. Real-time emulation for power equipment development. II. The virtual machine
 Slater, H.J.; Atkinson, D.J.; Jack, A.G.;
 <u>Electric Power Applications, IEE Proceedings-</u>
 Volume 145, Issue 3, May 1998 Page(s):153 - 158
 AbstractPlus Full Text: PDF(592 KB) IET JNL</p> |
| <input type="checkbox"/> | <p>3. A distributed computing approach for real-time transient stability analysis
 Aloisio, G.; Bochicchio, M.A.; La Scala, M.; Sbrizzai, R.;
 <u>Power Systems, IEEE Transactions on</u>
 Volume 12, Issue 2, May 1997 Page(s):981 - 987
 Digital Object Identifier 10.1109/59.589801
 AbstractPlus Full Text: PDF(816 KB) IEEE JNL
 Rights and Permissions</p> |
| <input type="checkbox"/> | <p>4. Parallelized direct execution simulation of message-passing parallel programs
 Dickens, P.M.; Heidelberg, P.; Nicol, D.M.;
 <u>Parallel and Distributed Systems, IEEE Transactions on</u>
 Volume 7, Issue 10, Oct. 1996 Page(s):1090 - 1105
 Digital Object Identifier 10.1109/71.539740
 AbstractPlus References Full Text: PDF(1880 KB) IEEE JNL
 Rights and Permissions</p> |
| <input type="checkbox"/> | <p>5. Exploiting method-level parallelism in single-threaded Java programs
 Chen, M.K.; Olukotun, K.;
 <u>Parallel Architectures and Compilation Techniques, 1998. Proceedings, 1998 International Confere</u>
 12-18 Oct. 1998 Page(s):176 - 184
 Digital Object Identifier 10.1109/PACT.1998.727190
 AbstractPlus Full Text: PDF(52 KB) IEEE CNF
 Rights and Permissions</p> |


[Home](#) | [Login](#) | [Logout](#) | [Access Information](#) | [Alt](#)

Welcome United States Patent and Trademark Office

☐ Search Results

BROWSE

SEARCH

IEEE XPLORE GUIDE

Results for "((virtual machine<and>emulation)) <and> (pyr >= 1913 <and> pyr <= 2001)"

Your search matched 169 of 1625854 documents.

A maximum of 100 results are displayed, 25 to a page, sorted by Relevance in Descending order.

e-mail

» Search Options

[View Session History](#)
[New Search](#)

Modify Search

☐ Check to search only within this results set

Display Format:

☒ Citation

☐ Citation & Abstract

» Key

IEEE JNL IEEE Journal or Magazine
 IET JNL IET Journal or Magazine
 IEEE CNF IEEE Conference Proceeding
 IET CNF IET Conference Proceeding
 IEEE STD IEEE Standard

[Select All](#) [Deselect All](#)

View: 1-25 |

- ☐ 1. **Real-time emulation for power equipment development. II. The virtual machine**
 Slater, H.J.; Atkinson, D.J.; Jack, A.G.;
Electric Power Applications, IEEE Proceedings-
 Volume 145, Issue 3, May 1998 Page(s):153 - 158
AbstractPlus | Full Text: [PDF\(592 KB\)](#) IET JNL
- ☐ 2. **Advances and future challenges in binary translation and optimization**
 Altman, E.R.; Ebcioglu, K.; Gschwind, M.; Sathaye, S.;
Proceedings of the IEEE
 Volume 89, Issue 11, Nov. 2001 Page(s):1710 - 1722
 Digital Object Identifier 10.1109/5.964447
AbstractPlus | [References](#) | Full Text: [PDF\(232 KB\)](#) | Full Text: [HTML](#) IEEE JNL
[Rights and Permissions](#)
- ☐ 3. **Special Feature: Microprogrammable Virtual Machines**
 Bagley, J.D.;
Computer
 Volume 9, Issue 2, Feb. 1976 Page(s):38 - 42
AbstractPlus | Full Text: [PDF\(3320 KB\)](#) IEEE JNL
[Rights and Permissions](#)
- ☐ 4. **A scalable and reusable emulator for evaluating the performance of SS7 networks**
 Lazar, A.A.; Tseng, K.H.; Koon Seng Lim; Choe, W.;
Selected Areas in Communications, IEEE Journal on
 Volume 12, Issue 3, April 1994 Page(s):395 - 404
 Digital Object Identifier 10.1109/49.285300
AbstractPlus | Full Text: [PDF\(916 KB\)](#) IEEE JNL
[Rights and Permissions](#)
- ☐ 5. **Logic emulation with virtual wires**
 Babb, J.; Tessier, R.; Dahl, M.; Hanono, S.Z.; Hoki, D.M.; Agarwal, A.;
Computer-Aided Design of Integrated Circuits and Systems, IEEE Transactions on
 Volume 16, Issue 6, June 1997 Page(s):609 - 626
 Digital Object Identifier 10.1109/43.640619
AbstractPlus | [References](#) | Full Text: [PDF\(520 KB\)](#) IEEE JNL
[Rights and Permissions](#)